

PATENT ASSIGNMENT COVER SHEET

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EPAS ID: PAT6043107

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
MONTEREY RESEARCH LLC	02/12/2020
RECEIVING PARTY DATA	
Name:	SPINDLETOP IP LLC
Street Address:	2108 DALLAS PKWY
Internal Address:	STE 214 - 1034
City:	PLANO
State/Country:	TEXAS
Postal Code:	75093
PROPERTY NUMBERS Total: 5	
Property Type	Number
Patent Number:	6191607
Patent Number:	6346795
Patent Number:	6819539
Patent Number:	6424570
Patent Number:	7221200
CORRESPONDENCE DATA	
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<i>Correspondence will be sent to the e-mail address first; if that is unsuccessful, it will be sent using a fax number, if provided; if that is unsuccessful, it will be sent via US Mail.</i>	
Phone:	2815074079
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Correspondent Name:	SPINDLETOP IP LLC
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NAME OF SUBMITTER:	TIN NGUYEN
SIGNATURE:	/Tin Nguyen/
DATE SIGNED:	04/02/2020
Total Attachments: 2	

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PATENT

REEL: 052290 FRAME: 0506

Exhibit A

PATENT ASSIGNMENT

For good and valuable consideration, the receipt of which is hereby acknowledged, Monterey Research LLC, a limited liability corporation located at 3946 Freedom Circle, Suite 900, Santa Clara, CA 95054 ("**Assignor**"), does hereby assign, transfer, and convey unto Spindletop IP LLC, a Texas limited liability company, having an address at 2108 Dallas Parkway, Suite 214 – 1034, Plano, Texas 75093-4362, ("**Assignee**"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "**Patent Rights**"):

(a) the patent applications and patents listed in the table below (the "**Patents**" or "**Patent**");

U.S. Pat No.	App/No	US Filing Dt	Title
6191607	09/153,941	9/16/98	Programmable bus hold circuit and method of using the same
6346795	09/769,344	1/26/01	Discharge control circuit of batteries
6819539	09/933,254	8/20/01	Method for circuit recovery from overstress conditions
6424570	09/892,189	6/26/01	Modulated charge pump with uses an analog to digital converter to compensate for supply voltage variations
7221200	11/075,632	3/8/05	Programmable low voltage reset apparatus for multi-Vdd chips

(b) any parents, patents that share the same specification as (a);

(c) any additional patents or applications to which (a) or (b) claims priority, as well as any additional patents and patent applications claiming priority to (a) or (b), including but not limited to patents or patent applications issued, granted, or pending via any divisionals, continuations, continuations-in-part, reissues and reexaminations of (a) and (b);

Exhibit A

(d) any patents or patent applications that are terminally disclaimed to any of the foregoing in (a) through (c) or to which any of the foregoing in (a) through (c) are terminally disclaimed;


(e) any foreign counterparts of (a) through (d), wherein all assets included in (a) through (e); and U.S. Patent No. 6,854,067, shall be herein referred to as "Patents and Related Patents";

(f) all right, title and interest to the Patents and Related Patents.


(g) all of the rights, privileges including the benefits of any attorney client privilege or attorney work product privilege, title and interest in and to the Patents and Related Patents sold, transferred, assigned and set over to Assignee hereunder including all income, royalties, damages and payments now or hereafter due or payable with respect thereto; and

(h) the right to bring any claim, sue, counterclaim, and recover for the past, present and future infringement of the rights assigned hereunder.

ASSIGNOR: MONTEREY RESEARCH LLC

By: 
Name: Keith M. Wilson
Title: General Manager
Date: 10 Feb 2020

ASSIGNEE: SPINDLETOP IP LLC

By: 
Name: Tink Nguyen
Title: Managing member
Date: 2/12/2020